IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

Morishita, Y.

Serial No.: 09/619,669

Group Art Unit: 2814

Filed: July 19, 2000

Examiner: Nguyen, Dilinh P.

For: AN INPUT/OUTPUT PROJECTION DEVICE FOR A SEMICONDUCTOR

INTEGRATED CIRCUIT

Honorable Assistant Commissioner of Patents Washington, D.C. 20231

PETITION AND FEE FOR EXTENSION OF TIME UNDER 37 C.F.R. 1.136(a)

Sir:

Applicant hereby petitions for a One-month extension of time to respond to the Office Action mailed December 16, 2002. A check for the extension fee in the amount of \$110.00 is attached. A duplicate copy of this sheet is submitted herewith.

The Commissioner is authorized to charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

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02 FC:1251

110.00 OP

Frederick E. Cooperrider, Esa

Respectfully submitted.

Reg. No. 36,769

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